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## United States Patent and Trademark Office

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UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER OF PATENTS AND TRADEMARKS Washington, D.C. 20231 www.uspta.gov

## NOTICE OF ALLOWANCE AND FEE(S) DUE

**EXAMINER** FITZPATRICK CELLA HARPER & SCINTO 30 ROCKEFELLER PLAZA LEE, WILSON NEW YORK, NY 10112 ART UNIT **CLASS-SUBCLASS** 315-169300 DATE MAILED: 04/09/2003 APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. 07/20/2001 09/909,016 Hisao Tajima 35.C15582 9097 TITLE OF INVENTION: ELECTRON-EMITTING DEVICE AND IMAGE FORMING APPARATUS ISSUE FEE PUBLICATION FEE APPLN. TYPE SMALL ENTITY TOTAL FEE(S) DUE DATE DUE

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. <u>PROSECUTION ON THE MERITS IS CLOSED</u>, THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

\$300

\$1300

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE REFLECTS A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE APPLIED IN THIS APPLICATION. THE PTOL-85B (OR AN EQUIVALENT) MUST BE RETURNED WITHIN THIS PERIOD EVEN IF NO FEE IS DUE OR THE APPLICATION WILL BE REGARDED AS ABANDONED.

#### HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

NO

B. If the status is changed, pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above and notify the United States Patent and Trademark Office of the change in status, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check the box below and enclose the PUBLICATION FEE and 1/2 the ISSUE FEE shown above.

\$1600

 Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

II. PART B - FEE(S) TRANSMITTAL should be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). Even if the fee(s) have already been paid, Part B - Fee(s) Transmittal should be completed and returned. If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Box ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

## PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Box ISSUE FEE

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						(Signature
						(Date
APPLICATION NO.	FILING DATE	FIRS	T NAMED INVEN	TOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/909,016	07/20/2001	<u> </u>	Hisao Tajima		35.C15582	9097
APPLN. TYPE nonprovisional	SMALL ENTITY NO	ISSUE FEE \$1300	PUBLI	CATION FEE	TOTAL FEE(S) DUE \$1600	DATE DUE 07/09/2003
EXAMIN	ER ~	ART UNIT	CLASS-SUBCL	ASS		
LEE, WIL	SON	2821	315-16930	0		
1. Change of correspondenc CFR 1.363).	e address or indication of	"Fee Address" (37	2. For printing the names of up	on the patent from	t page, list (1)	
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☐ "Fee Address" indication PTO/SB/47; Rev 03-02 or Number is required.	n (or "Fee Address" Indica more recent) attached. U	ation form se of a Customer	registered paten	t attorneys or ager e will be printed.		
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Please check the appropriate a	assignee category or categ	ories (will not be printed	on the patent)	☐ individual	☐ corporation or other private gr	oun entity
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/909,016	07/20/2001	Hisao Tajima	35.C15582		
05514 7:	590 04/09/2003		EXAMIN	ER	
FITZPATRICK CELLA HARPER & SCINTO 30 ROCKEFELLER PLAZA			LEE, WILSON		
NEW YORK, NY 10112			ART UNIT	PAPER NUMBER	
			2821		
			TE MAILED: 04/09/2003		

# Determination of Patent Term Adjustment under 35 U.S.C. 154 (b) (application filed on or after May 29, 2000)

The patent term adjustment to date is 11 days. If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the term adjustment will be 11 days.

If a continued prosecution application (CPA) was filed in the above-identified application, the filing date that determines patent term adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) system. (http://pair.uspto.gov)

Any questions regarding the patent term extension or adjustment determination should be directed to the Office of Patent Legal Administration at (703)305-1383.



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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/909,016	07/20/2001	Hisao Tajima	35.C15582 9097 EXAMINER		
05514	7590 04/09/200				
FITZPATRICK CELLA HARPER & SCINTO			LEE, WILSON		
30 ROCKEFELI NEW YORK, N			ART UNIT	PAPER NUMBER	
UNITED STATES			2821		
			DATE MAILED: 04/09/2003		

## Notice of Fee Increase on January 1, 2003

If a reply to a "Notice of Allowance and Fee(s) Due" is filed in the Office on or after January 1, 2003, then the amount due will be higher than that set forth in the "Notice of Allowance and Fee(s) Due" since there will be an increase in fees effective on January 1, 2003. See Revision of Patent and Trademark Fees for Fiscal Year 2003; Final Rule, 67 Fed. Reg. 70847, 70849 (November 27, 2002).

The current fee schedule is accessible from: http://www.uspto.gov/main/howtofees.htm.

If the issue fee paid is the amount shown on the "Notice of Allowance and Fee(s) Due," but not the correct amount in view of the fee increase, a "Notice to Pay Balance of Issue Fee" will be mailed to applicant. In order to avoid processing delays associated with mailing of a "Notice to Pay Balance of Issue Fee," if the response to the Notice of Allowance and Fee(s) due form is to be filed on or after January 1, 2003 (or mailed with a certificate of mailing on or after January 1, 2003), the issue fee paid should be the fee that is required at the time the fee is paid. If the issue fee was previously paid, and the response to the "Notice of Allowance and Fee(s) Due" includes a request to apply a previously-paid issue fee to the issue fee now due, then the difference between the issue fee amount at the time the response is filed and the previously paid issue fee should be paid. See Manual of Patent Examining Procedure, Section 1308.01 (Eighth Edition, August 2001).

Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at (703) 305-8283.

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	Application No.	Applicant(s)	
Al di CAllana bilita	09/909,016	TAJIMA ET AL.	
Notice of Allowability	Examiner	Art Unit	- "
	Wilson Lee	2821	
The MAILING DATE of this communication appeal all claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R	(OR REMAINS) CLOSED in the or other appropriate communicement. This application is sul	his application. If not included ication will be mailed in due co	urse. THIS
1. This communication is responsive to 1/2/03.			
2. The allowed claim(s) is/are 1-137.	venin er		
<ul> <li>The drawings filed on 20 July 2001 are accepted by the Extension</li> <li>Acknowledgment is made of a claim for foreign priority under the content of the</li></ul>		A.	
a) ⊠ All b) ☐ Some* c) ☐ None of the:	del 33 0.3.0. § 1 19(a)-(u) oi (i	·/·	
<ol> <li>Certified copies of the priority documents have</li> </ol>	e been received.		
<ol><li>Certified copies of the priority documents have</li></ol>	e been received in Application	No	
<ol> <li>Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)).</li> </ol>	cuments have been received i	n this national stage application	n from the
* Certified copies not received:			
5. Acknowledgment is made of a claim for domestic priority u	- ,,,	provisional application).	
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o.  Acknowledgment is made of a claim for domestic phonty to	nuel 33 0.3.C. 99 120 and/or	121.	
Applicant has THREE MONTHS FROM THE "MAILING DATE" of below. Failure to timely comply will result in ABANDONMENT of			
7.  A SUBSTITUTE OATH OR DECLARATION must be submINFORMAL PATENT APPLICATION (PTO-152) which gives reas			TICE OF
8. CORRECTED DRAWINGS must be submitted.  (a) including changes required by the Notice of Draftsper  1) hereto or 2) to Paper No	•	,	
(b) including changes required by the proposed drawing			
(c) including changes required by the attached Examiner	's Amendment / Comment or i	n the Office action of Paper No	)
Identifying indicia such as the application number (see 37 CFR 1 of each sheet. The drawings should be filed as a separate paper			the back)
<ol> <li>DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT FOR T</li> </ol>	sit of BIOLOGICAL MATEF HE DEPOSIT OF BIOLOGICA	RIAL must be submitted. Not AL MATERIAL.	te the
Attachment(s)			
1⊠ Notice of References Cited (PTO-892)		nformal Patent Application (PT	•
<ul><li>3☐ Notice of Draftperson's Patent Drawing Review (PTO-948)</li><li>5☒ Information Disclosure Statements (PTO-1449), Paper No. 7</li></ul>		Summary (PTO-413), Paper No s Amendment/Comment	D
7		s Amendment Comment s Statement of Reasons for Alk	owance
of Biological Material	9 ☐ Other		
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Don Wo			
Supervisory Pate	n Examiner		
Technology Ce	mer 2800		

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## Reasons for allowance

Claims 1-137 are allowed.

The following is an examiner's statement of reasons for allowance:

The prior art neither discloses nor suggests an electron-emitting apparatus comprising an acceleration electrode mounted at a position facing the electron source substrate, the acceleration electrode being applied with an acceleration potential for accelerating electrons emitted from the electron-emitting devices; a potential supply path for supplying the acceleration potential to the acceleration electrode, the potential supply path being introduced via an intermediate area on the side of the electron source substrate further comprising:

a first wire formed around the intermediate area; a resistor film formed between the first wire and the intermediate area, the resistor film electrically connected with the potential supply path and the first wire such as required by claim 1;

a first wire provided separately from the driving wires and formed on a surface between the intermediate area and the driving wires; and resistor film formed on a surface between the first wire and the intermediate area, the resistor film electrically connected with the potential supply path and the first wire such as required by claim 4;

a first wire provided separately from the driving wires and formed on a surface between the intermediate area and the driving wires; and a periodical projection/recess structure formed on a surface between the first wire and the intermediate area such as required by claim 39;

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a first wire provided separately from the driving wires and formed on a surface between a passing area and driving wires; a sealing structure integrated with the potential supply path and hermetically mounted in a hole formed through the electron source substrate; and a projection/recess structure formed on a surface between the sealing structure and the first wire such as required by claim 40;

a first wire provided separately from the driving wires and formed on a surface between the intermediate area and the driving wires; and a second wire provided separately from the acceleration electrode around the acceleration electrode on the acceleration electrode substrate, wherein a space surrounded by the electron source substrate, the acceleration electrode substrate and a peripheral frame is maintained as a vacuum atmosphere, a lead portion of the first wire is extended outside of the vacuum atmosphere, a lead portion of the second wires is extended outside of the vacuum atmosphere, and a conductive contact member is in contact with the lead portions of the first and second wires such as required by claim 111.

a potential supply path for supplying the acceleration potential to the acceleration electrode, at least a portion of the potential supply path passing through the electron source substrate, a first wire provided separately from the driving wires and formed on a surface between the portion of the potential supply path and the driving wires, and a resistor film formed on a surface between the first wire and the portion of the potential supply path, and the resistor film being electrically connected with the potential supply path and the first wire such as required by claim 130;

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a potential supply path for supplying the acceleration potential to the acceleration electrode, at least a portion of the potential supply path passing through the electron source substrate, a first wire provided separately from the driving wires and formed on a surface between the portion of the potential supply path and the driving wires, and a periodical projection/recess structure formed on a surface between the first wire and the portion of the potential supply path such as required by claim 131;

a potential supply path for supplying the acceleration potential to the acceleration electrode, at least a portion of the potential supply path passing through the electron source substrate, a first wire provided separately from the driving wires and formed on a surface between the portion of the potential supply path and the driving wires, a sealing structure integrated with the potential supply path and hermetically mounted in a hole formed through the electron source substrate, and a periodical projection/recess structure formed on a surface between the first wire and the portion of the potential supply path such as required by claim 132;

a potential supply path for supplying the acceleration potential to the acceleration electrode, at least a portion of the potential supply path passing through the electron source substrate, a first wire provided separately from the driving wires and formed on a surface between the portion of the potential supply path and the driving wires, a second wire provided separately from the acceleration electrode around the acceleration electrode on the acceleration electrode substrate, wherein a space surrounded by the electron source substrate, the acceleration electrode substrate and a peripheral frame is maintained as having a vacuum atmosphere, a lead portion of the first wire is extended

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outside of the vacuum atmosphere, and a conductive contact member is in contact with the lead portions of the first and second wires such as required by claim 133;

driving wires connected to the electron-emitting devices, an electron source substrate on which the electron-emitting device and the driving wires and arranged, wherein on the substrate is provided a portion to which an acceleration potential for accelerating electrons emitted from the electron-emitting devices is supplied, a first wire provided separately from the driving wires and formed on a surface between the portion and the driving wires, and a resistor film formed a surface such as required by claim 134;

driving wires connected to the electron-emitting devices, an electron source substrate on which the electron-emitting device and the driving wires and arranged, wherein on the substrate is provided a portion to which an acceleration potential for accelerating electrons emitted from the electron-emitting devices is supplied, an electro-conductive film provided separately from the driving wires and formed on a surface between the portion and the driving wires, and a resistor film formed on a surface between the first wire and the portion such as required by claim 135;

driving wires connected to the electron-emitting devices, an electron source substrate on which the electron-emitting device and the driving wires and arranged, wherein on the substrate is provided a portion to which an acceleration potential for accelerating electrons emitted from the electron-emitting devices is supplied, a first wire provided separately from the driving wires and formed on a surface between the portion

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and the driving wires, and a periodical projection/recess structure formed on a surface between the first wire and the portion such as required by claim 136;

driving wires connected to the electron-emitting devices, an electron source substrate on which the electron-emitting device and the driving wires and arranged, wherein on the substrate is provided a portion to which an acceleration potential for accelerating electrons emitted from the electron-emitting devices is supplied, an electro-conductive film provided separately from the driving wires and formed on a surface between the portion and the driving wires, and a periodical projection/recess structure formed on a surface between the first wire and the portion such as required by claim 137.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Todokoro (6,342,875) discloses an image-forming apparatus comprising an acceleration electrode, potential supply, electron emitting devices, interlocking layer, resistor. Sanou et al. (6,278,233) discloses an image-forming apparatus with spacer comprising a plurality of recess portion for inserting insulating spacers, an acceleration electrode, electron emitting devices.

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# Correspondence

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Examiner Wilson Lee whose telephone number is (703) 306-3426.

Any inquiry of a general nature or relating to the status of this application should be directed to the Technology Center receptionist whose telephone number is (703) 308-0956.

Papers related to Technology Center 2800 applications may be submitted to Technology Center 2800 by facsimile transmission. Any transmission not to be considered an official response must be clearly marked "DRAFT". The Technology Center Fax number is (703) 308-7722 or (703) 308-7724.

WL

4/7/03

Don Wong
Supervisory Patent Examine
Technology Certer 2800